# Job Description: Engineer - FPGA

| i. General Information | |
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| **Role: Engineer - FPGA** | **Department:  EO** |
| **Division:  Gurugram** | **Job Grade/Level:** |
| **Functional Reporting: Dr. Vishal Chaudhary** | **Administrative Reporting: Prachi Ma'am** |

| ii. Job Objective |
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| We are looking for a FPGA design engineer for developing the electronics of electro-optical devices. |

### iii. Key Responsibility Areas

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| **Strategy** | <ul><li>Design and Development of low power, low latency, image processing electronics.</li><li>Be responsible for Electronic Logic design and coding in Hardware Description Language on FPGA</li><li>Create FPGA validation environment</li><li>Set up the test procedures and test benches for design validation</li><li>Run simulations to ensure the desired results are being achieved</li><li>Efficiently carry out debugging activities</li><li>Create detailed design documents</li><li>Maintain records of all the development activities</li><li>Interact and Engage with external developers/partner to successfully complete the design project.</li></ul> |
| **Planning and Execution** | |
| **Reporting and Coordination** | ▪ |
| **Development** | ▪ |

### iv. Key Performance Indicators

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|---|---|
| **Productivity Improvement** | |
| **Quality** | |
| **Delivery** | |
| **Cost Control** | |
| **Compliance** | |

### v. Stakeholder Interactions

| Type of interaction | Interaction with | Nature of interaction |
|---|---|---|
| **Internal** | | |

| Internal | | |
|---|---|---|
| External | | |

| vi. Job Specifications | | |
|---|---|---|
| **Qualification** | Min. qualification required | B.Tech. / M.Tech. Electrical and Electronics or similar field. |
| | Other desired qualifications | |
| **Relevant Experience** | Min. no. of years of relevant experience required | • 5-6 years of experience in domain |
| **Knowledge and Skills Required** | | • Sound Verilog/VHDL knowledge<br>• Sound knowledge of synthesis along with place & route<br>• Should have experience of working on Xilinx<br>• Preferably have worked on Lattice<br>• Should be proficient in English |